

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	0	compar\$6 and 480214.pn.	USPA T	2003/05/29 12:34	
2	BRS	L2	1	compar\$6 and 4802174.pn.	USPA T	2003/05/29 13:25	
3	BRS	L3	489	(plural\$3 near2 delay\$3 or multi\$3 near2 delay\$3 or several near2 delay\$3 or parallel near3 delay\$3 or first adj1 delay\$3 near4 second adj1 delay\$1) same (clock\$3 near2 signal\$1 or clock\$3 near2 generat\$4 or clock\$3 near2 master\$1 or clock\$3 near3 pulse\$1 or VCO near3 clock\$3) same (invert\$3 or driver\$1 or buffer\$1)	USPA T	2003/05/29 13:32	
4	BRS	L4	145	(plural\$3 near2 sampl\$3 or multi\$3 near2 sampl\$3 or several near2 sampl\$3 or parallel near3 sampl\$3 or first adj1 sampl\$3 near4 second adj1 sampl\$3) same (clock\$3 near2 signal\$1 or clock\$3 near2 generat\$4 or clock\$3 near2 master\$1 or clock\$3 near3 pulse\$1 or VCO near3 clock\$3) same (invert\$3 or driver\$1 or buffer\$1)	USPA T	2003/05/29 13:36	

	Error Definition	Er ro rs
1		0
2		0
3		0
4		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
5	BRS	L5	0	(plural\$3 near2 "S/H" or multi\$3 near2 "S/H" or several near2 "S/H" or parallel near3 "S/H" or first adj1 "S/H" near4 second adj1 "S/H") same (clock\$3 near2 signal\$1 or clock\$3 near2 generat\$4 or clock\$3 near2 master\$1 or clock\$3 near3 pulse\$1 or VCO near3 clock\$3) same (invert\$3 or driver\$1 or buffer\$1)	USPA T	2003/05/2 9 13:37	
6	BRS	L6	374	(plural\$3 near2 flip\$1 adj1 flop\$1 or multi\$3 near2 flip\$1 adj1 flop\$1 or several near2 flip\$1 adj1 flop\$1 or parallel near3 flip\$1 adj1 flop\$1 or first adj1 flip\$1 adj1 flop\$1 near4 second adj1 flip\$1 adj1 flop\$1) same (clock\$3 near2 signal\$1 or clock\$3 near2 generat\$4 or clock\$3 near2 master\$1 or clock\$3 near3 pulse\$1 or VCO near3 clock\$3) same (invert\$3 or driver\$1 or buffer\$1)	USPA T	2003/05/2 9 13:39	

	Error Definition	Er ro rs
5		0
6		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
7	BRS	L9	0	(combin\$3 near3 delay\$3 or summ\$3 near3 delay\$3 or add\$3 near3 delay\$3 or combin\$3 near3 driv\$3 or summ\$3 near3 driv\$3 or add\$3 near3 driv\$3 or combin\$3 near3 sampl\$3 or summ\$3 near3 sampl\$3 or add\$3 near3 sampl\$3 or summ\$3 near3 logic near1 circuit\$3 or add\$3 near3 logic near2 circuit\$3 or combin\$3 near3 logic near1 circuit\$3) and 5	USPA T	2003/05/2 9 13:43	
8	BRS	L7	153	(combin\$3 near3 delay\$3 or summ\$3 near3 delay\$3 or add\$3 near3 delay\$3 or combin\$3 near3 driv\$3 or summ\$3 near3 driv\$3 or add\$3 near3 driv\$3 or combin\$3 near3 sampl\$3 or summ\$3 near3 sampl\$3 or add\$3 near3 sampl\$3 or summ\$3 near3 logic near1 circuit\$3 or add\$3 near3 logic near2 circuit\$3 or combin\$3 near3 logic near1 circuit\$3) and 3	USPA T	2003/05/2 9 13:46	

	Error Definition	Er ro rs
7		0
8		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
9	BRS	L8	57	(combin\$3 near3 delay\$3 or summ\$3 near3 delay\$3 or add\$3 near3 delay\$3 or combin\$3 near3 driv\$3 or summ\$3 near3 driv\$3 or add\$3 near3 driv\$3 or combin\$3 near3 sampl\$3 or summ\$3 near3 sampl\$3 or add\$3 near3 sampl\$3 or summ\$3 near3 logic near1 circuit\$3 or add\$3 near3 logic near2 circuit\$3 or combin\$3 near3 logic near1 circuit\$3) and 4	USPA T	2003/05/2 9 15:23	
10	BRS	L10	44	(combin\$3 near3 delay\$3 or summ\$3 near3 delay\$3 or add\$3 near3 delay\$3 or combin\$3 near3 driv\$3 or summ\$3 near3 driv\$3 or add\$3 near3 driv\$3 or combin\$3 near3 sampl\$3 or summ\$3 near3 sampl\$3 or add\$3 near3 sampl\$3 or summ\$3 near3 logic near1 circuit\$3 or add\$3 near3 logic near2 circuit\$3 or combin\$3 near3 logic near1 circuit\$3) and 6	USPA T	2003/05/2 9 15:27	

	Error Definition	Er ro rs
9		0
10		0